

MAIL STOP AMENDMENT  
PATENT  
8008-1052

**IN THE U.S. PATENT AND TRADEMARK OFFICE**

In re application of

Yasutaka NAKASHIBA Conf. 2273

Application No. 10/812,282 Group 2815

Filed March 30, 2004 Examiner J. Jackson, Jr.

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

**AMENDMENT**

Assistant Commissioner for Patents August 4, 2010  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Official Action of March 8, 2010, for which the period for response has been extended two months, please amend the above-identified application as follows:

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks** begin on page 9 of this paper.